Schedule

Tuesday	Topic
9:00	Intro & Quiz, Limitations of parallelism
	Multicore & computer architecture (I)
10:30	Coffee break
10:45	Multicore & computer architecture (II)
11:15	Exercise: likwid-topology, likwid-pin, STREAM
12:30	Lunch
13:30	Microbenchmarking
14:45	Exercise: FP divide latency, Monte Carlo π
15:30	Coffee break
15:45	The Roofline model
17:00	End of day 1

Schedule

Wednesday	Topic
9:00	Performance engineering with patterns
10:30	Coffee break
10:45	Optimal use of parallel resources
11:30	Exercise: Jacobi smoother
12:30	Lunch
13:30	Case study: Sparse MVM
14:30	Extending Roofline: The ECM model
15:15	Coffee break
15:30	Exercise: Code optimization
17:00	End of day 2